

## NANOSIL - FSP 2.1 “Silicon Nanowires”

One-day Workshop  
Louvain-la-Neuve, Belgium  
March 25<sup>th</sup>, 2010

Time	Speaker	Title
9:15 – 9:30	Prof. J.-P. Raskin	Introduction – objectives - Welcome
9:30 – 10:00	Prof. J.-P. Colinge	Junctionless Si NWs
10:00 – 10:20	Dr. G. Larrieu	Vertical Si NWs arrays for nanoelectronics
10:20 – 10:40	Prof. E. Dubois	Confined and guided catalytic growth of silicon nano-blades on an amorphous substrate
10:40 – 11:00	Break	
11:00 – 11:30	Dr. Sebastian Feste	Strained Si NWs
11:30 – 12:00	Dr. Susanna Reggiani	Modeling of Si NW FETs
12:00 – 12:30	Prof. Gerard Ghibaudo	Electrical characterization of Si NWs
12:30 – 13:30	Lunch	
13:30 – 14:00	Prof. J.-P. Raskin	Analog/RF performance of Si NWs including self-heating
14:00 – 14:30	Prof. A. M. Ionescu, Dr. D. Grogg	Resonant silicon micro/nanodevices
14:30 – 15:15	Invited speaker Dr. Bernard Capraro, Intel	Driving Moore’s Law into the future through Nano- electronics
15:15 – 16:30	Discussions Common actions Exchanges of devices for characterization and modelling Modelling to support characterization analyses	